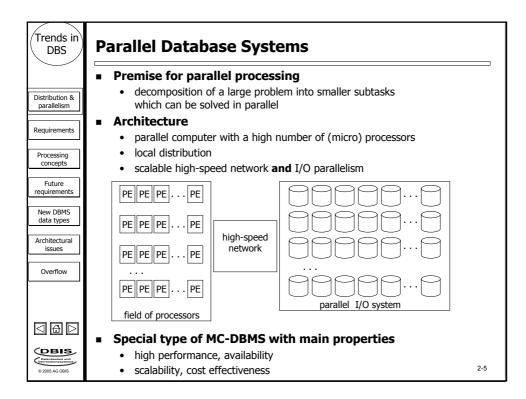
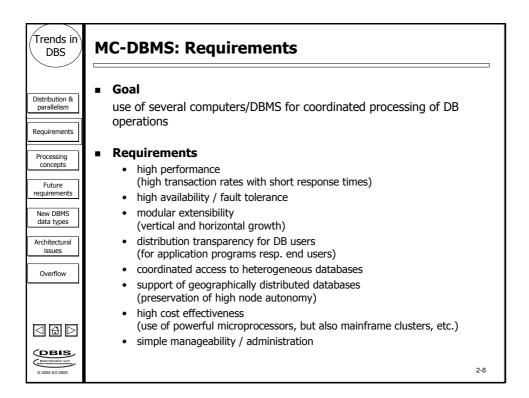
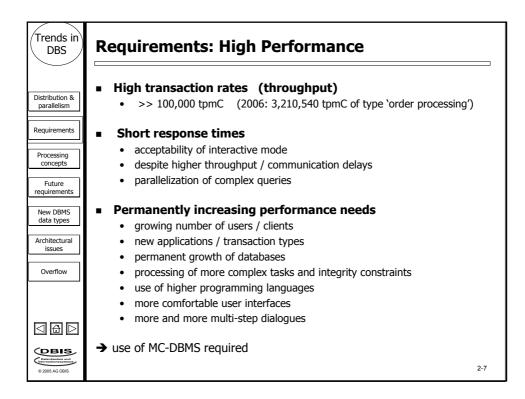
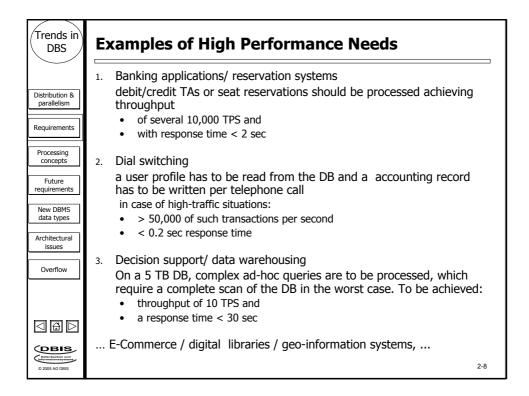


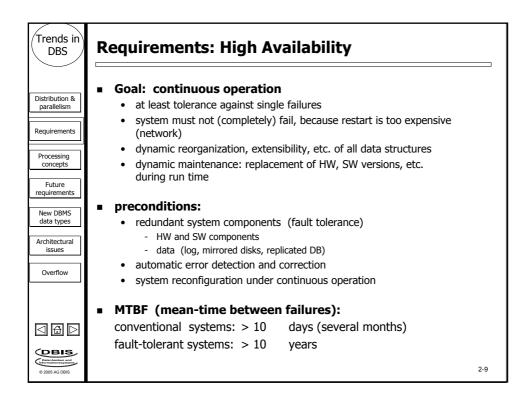
Trends in DBS	Distribution and Parallelism	
Distribution & parallelism Requirements Processing concepts	 Typical performance criteria for sequential processing: disk searching (table scan) MB/s sorting 0.1 MB/s join Processing time for a 10 TB database ? 	
Future requirements New DBMS data types Architectural issues Overflow	 Intra-transaction parallelism: use of parallelism within transactions operations on large tables: scan, join computation, sorting, creation of indexes, etc. full-text search in digital libraries multimedia applications complex logic programs, ••• short response times for data- and/or computation-intensive DB requests 	
	 Inter-transaction parallelism: high transaction rates for OLTP linear growth of throughput 	2-4

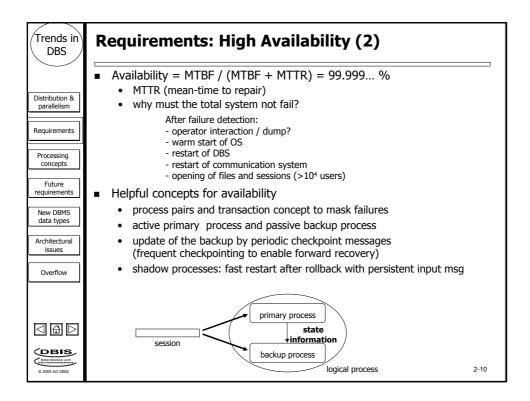




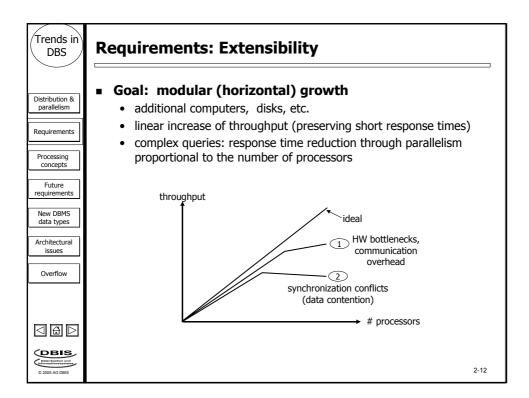


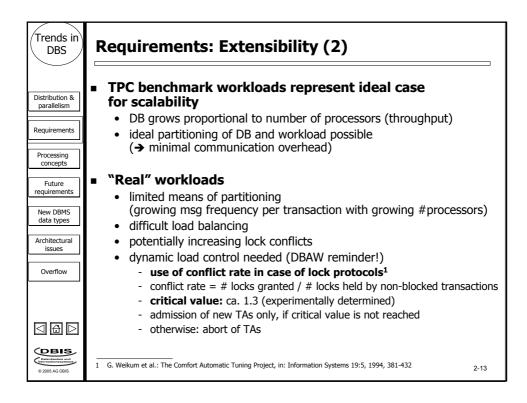




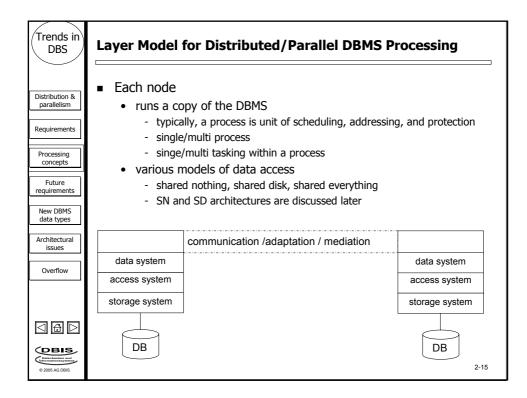


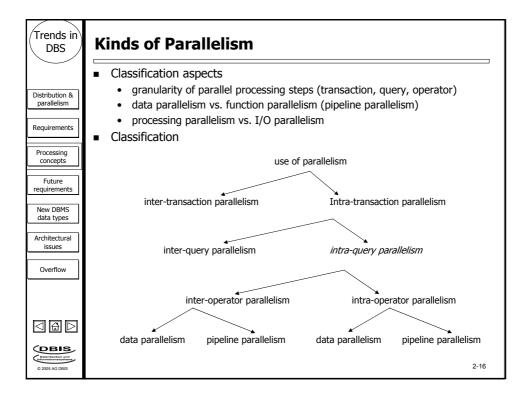
Trends in DBS	Analysis of Fail	ure Causes							
	 Classification of failure causes¹: 								
Distribution & parallelism	cause	1985	1989	today					
	software	33 %	62 %	>> %					
Requirements	hardware	29 %	7 %	<< %					
Dressesing	maintenance	19 %	5 %	<< %					
Processing concepts	operations	9 %	15 %	>> %					
Future	environment	6 %	6 %	<< %					
requirements	system MTBF	8 years	21 years	? years					
New DBMS data types	 "Under-reporting" for 	operating, applicat	ion SW						
Architectural issues	 SW errors are dominating cause of failure fast growing amount of application and system SW increasing SW complexity 								
Overflow	 Strong improvement f 	•	nance						
	U 1			ommunication via fiber					
	5 /	isks majority of all fail							
	 MTBF for disks implied 	roved from 8K to >10	0K hours!						
	 Larger degree of automation required for operating 								
Desenbarken und Desenbarken un	1. Gary: A Census of Tandem System Availability Between 1985 and 1990: in IEEE Trans. on Reliability 39 (4), 1990, 409-418. Recent figures are not available. The principal statements are still valid. 2-11								

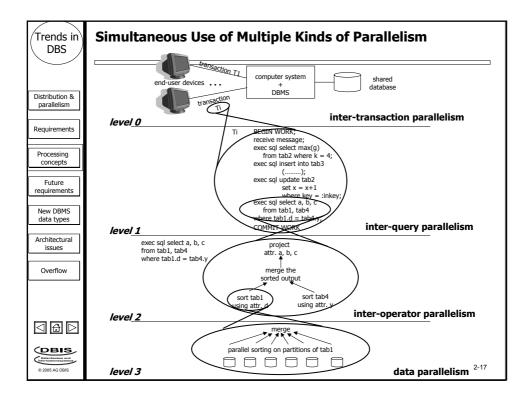


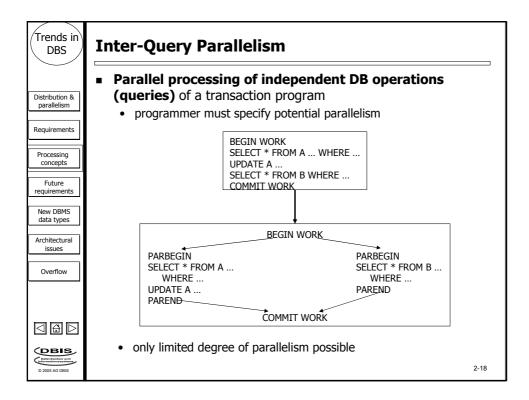


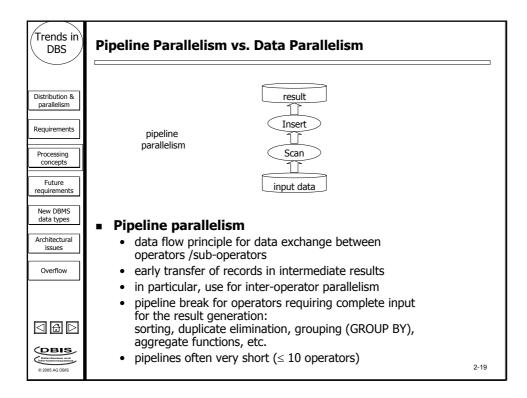
Trends in DBS	Relative Reference Matrix (Practical Example)														
	ca. 17 500 transactions, 1 million page references to ca. 66 000 different pages														
Distribution & parallelism		P1	P2	P3	P4	P5	P6	P7	P8	P9	P10	P11	P12	P13	Total
]	TT1	9.1	3.5	3.3		5.0	0.9	0.4	0.1				0.0		22.3
Requirements	TT2	7.5	6.9	0.4	2.6	0.0	0.5	0.8	1.0	0.3	0.2	0.0			20.3
	TT3	6.4	1.3	2.8	0.0	2.6	0.2	0.7	0.1	1.1	0.4		0.0	0.0	15.6
Processing concepts	TT4	0.0	3.4	0.3	6.8			0.6	0.4			0.0			11.6
	TT5	3.1	4.1	0.4		0.0		0.5	0.0						8.2
Future requirements	TT6	2.4	2.5	0.6		0.7		0.9	0.3						7.4
	TT7	1.3		2.6			2.3	0.1							6.2
New DBMS data types	TT8	0.3	2.3	0.2		0.0		0.1							2.9
duta types	TT9	0.0	1.4	0.0					1.1						2.6
Architectural	TT10	0.3	0.1	0.3			1.0	0.1					0.0		1.8
issues	TT11		0.9						0.2						1.1
Overflow	TT12		0.1												0.1
	Total	30.3	26.6	11.0	9.4	8.3	4.9	4.1	3.3	1.4	0.6	0.0	0.0	0.0	100.0
	partition size (%)	31.3	6.3	8.3	17.8	1.0	20.8	2.6	7.3	2.6	1.3	0.8	0.0	0.0	100.0
	% refe- renced	11.1	16.6	8.0	2.5	18.1	1.5	9.5	4.4	5.2	2.7	0.2	13.5	5.0	6.9
OBIS					-	→ Th	is is I	not u	inifor	m!					
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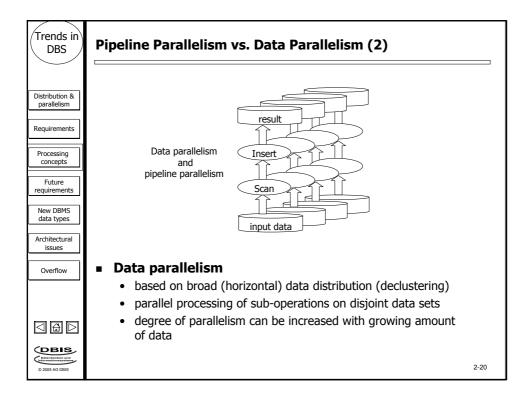


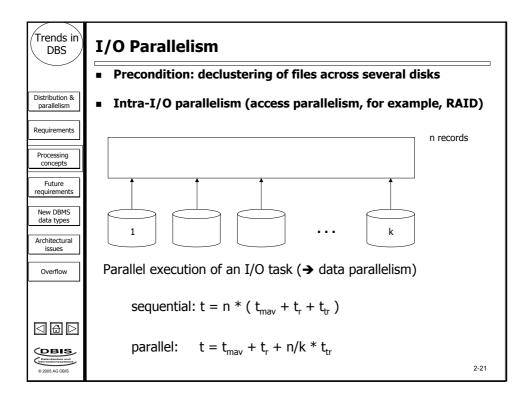


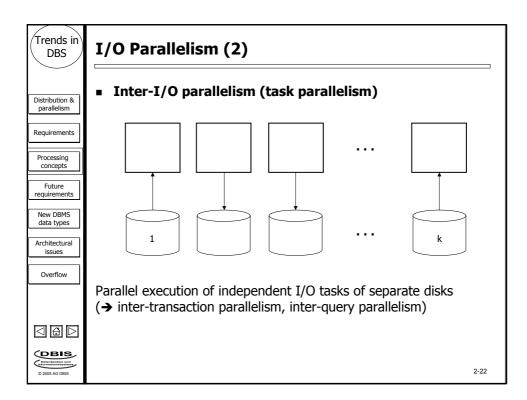


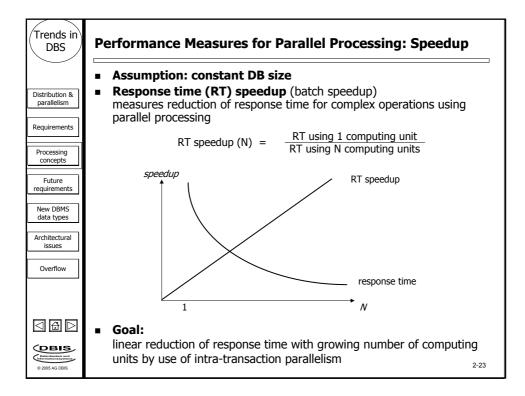


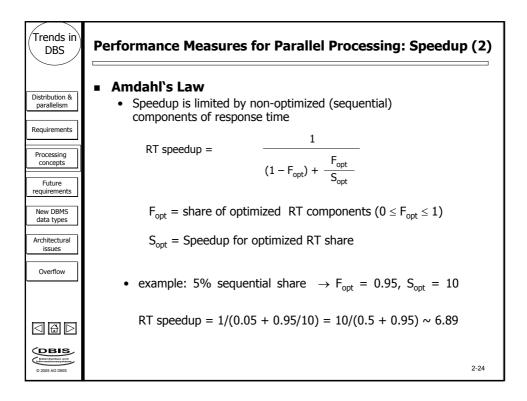


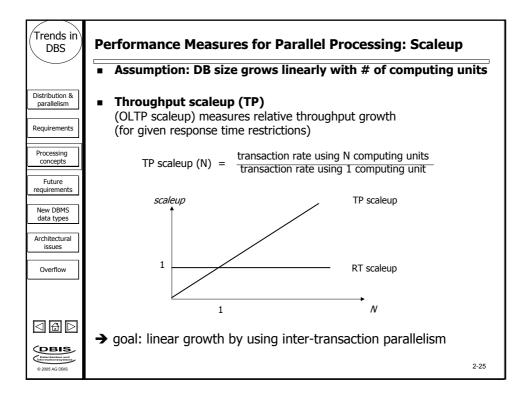


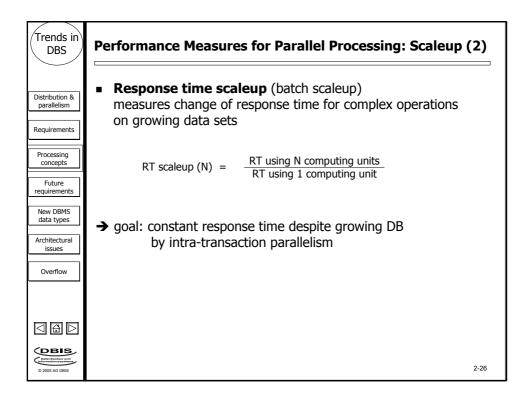


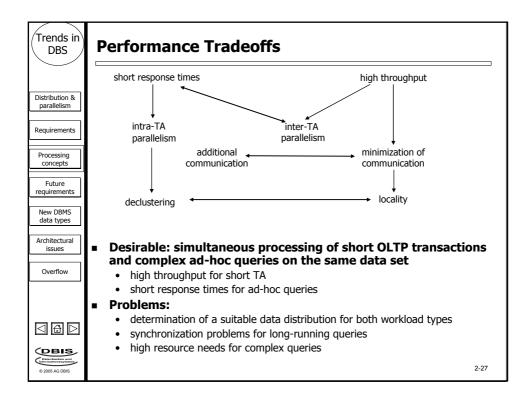


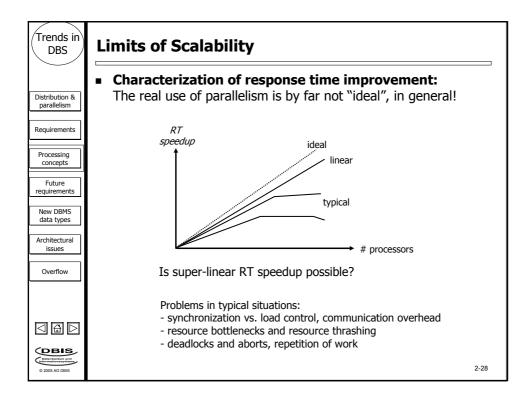




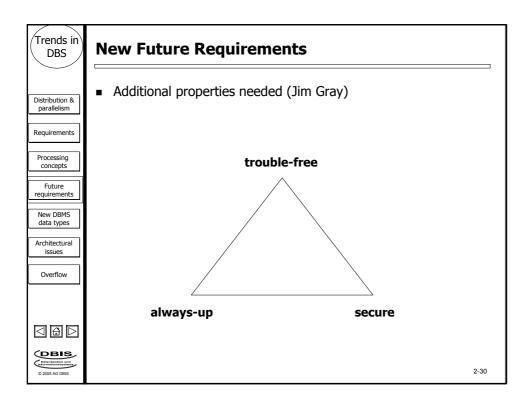


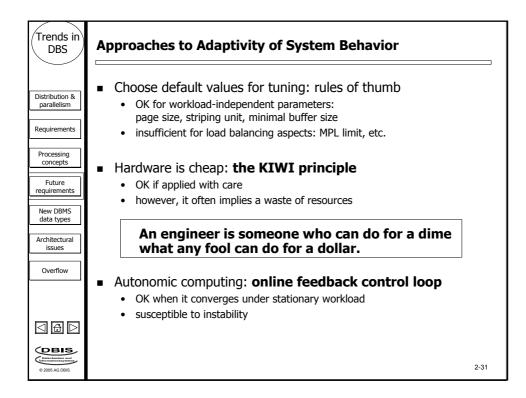


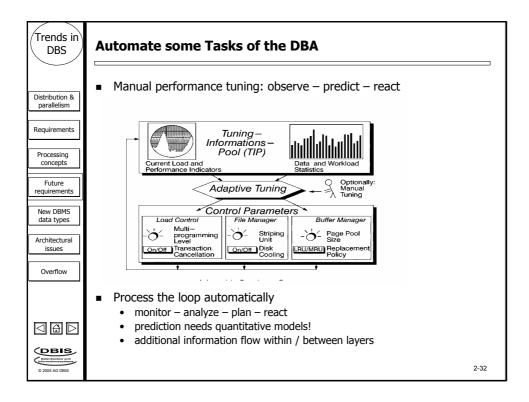


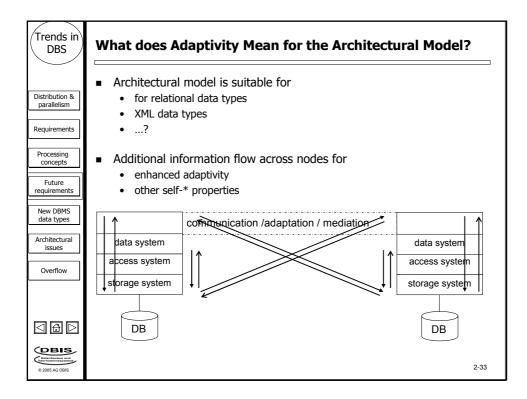


Trends in DBS	Limits of Scalability (2)					
Distribution & parallelism	 maximal inherent parallelism is limited startup- and termination overhead interferences of physical and logical resources variance (skew) in the execution times of subtasks 					
Processing concepts Future requirements	no skew skew					
New DBMS data types Architectural issues	time requirements of subtasks:					
Overflow	time requirement of task:					
	→ Amdahl's law limits response time speedup!					
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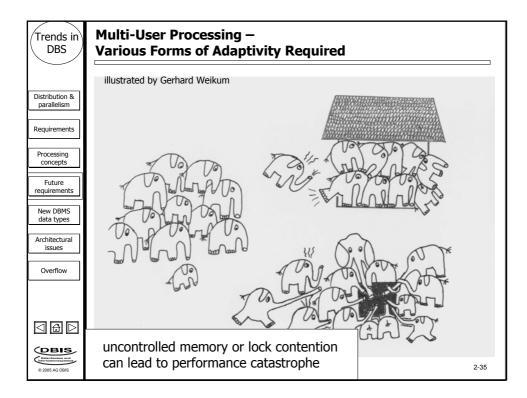


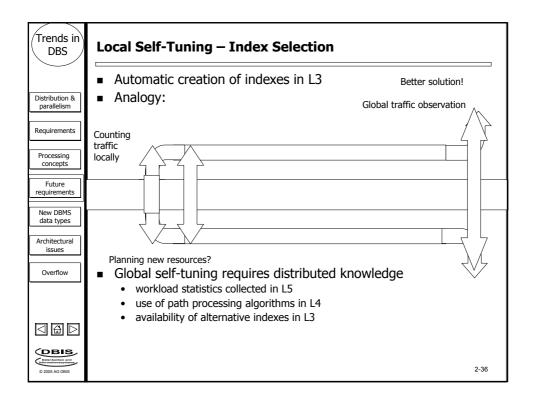


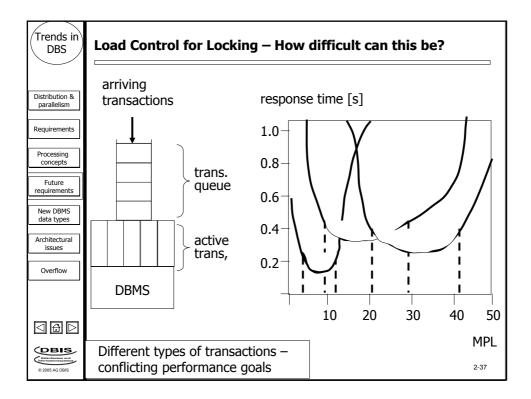


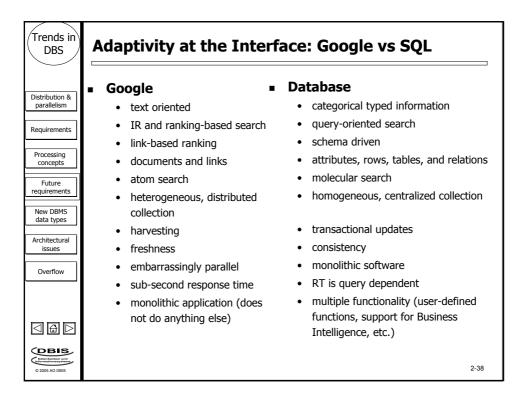


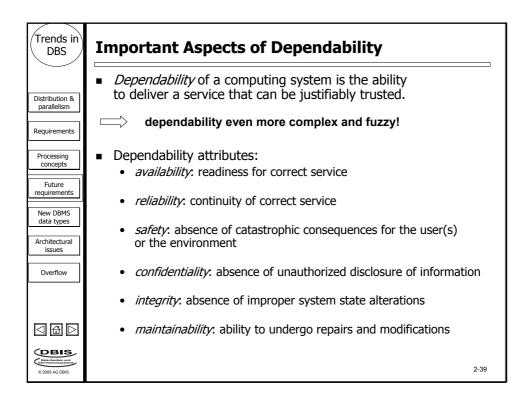
Trends in DBS	New Ta	asks to be Solved i	in the Kernel/Acr	oss No	odes
Distribution &		Transaction Mgmt	Query Processing	_	
Requirements	С	Consistency Control	Compilation Optimization	L5	Data System
Processing		Transaction Services	Path Processing Algorithms	L4	
Future requirements	I	Concurrency Control	Document Representation Labeling, Indexing	L3	Access System
Architectural issues	A D	Logging / Recovery	Buffer Mgmt Propagation Control File Services	L2 L1	Storage System
Overflow					
		Examples: buffer managemer index selection (L3 load balancing (L3			
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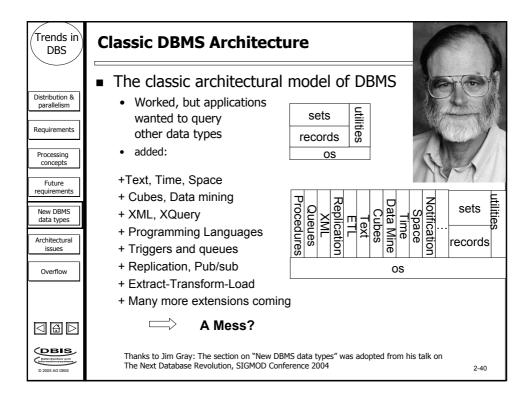


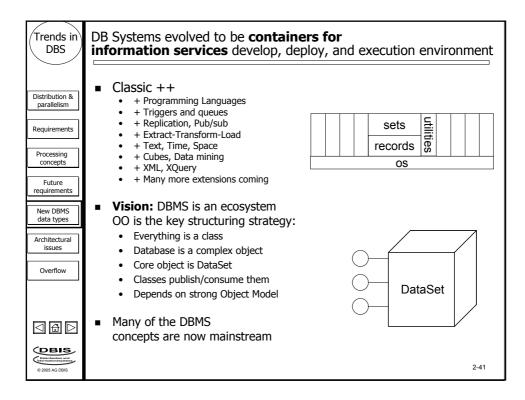


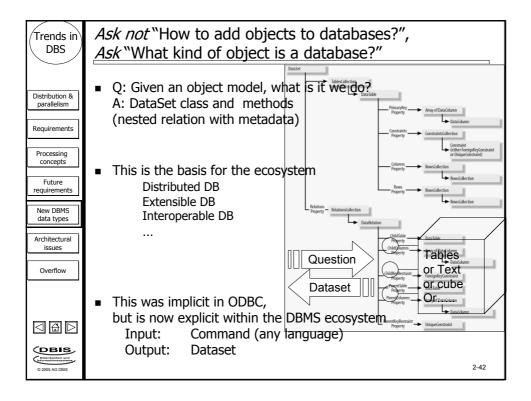


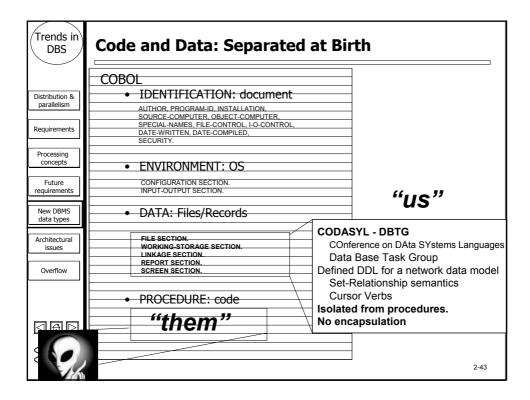


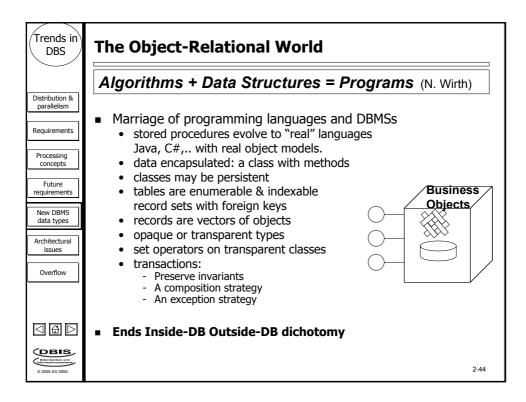


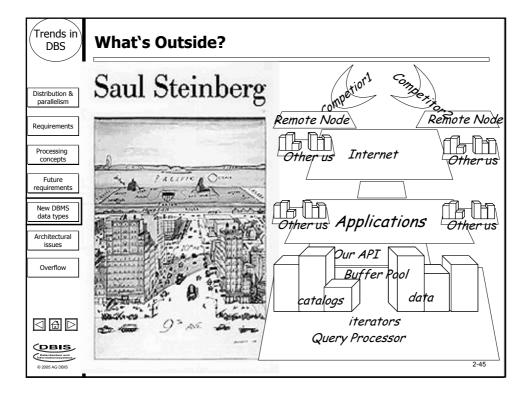


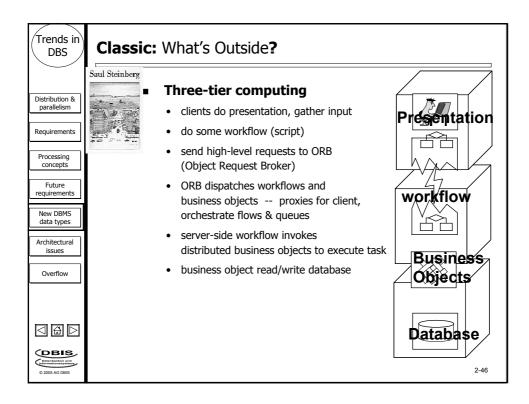


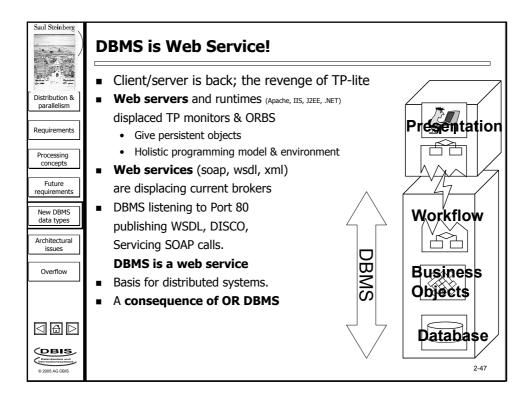


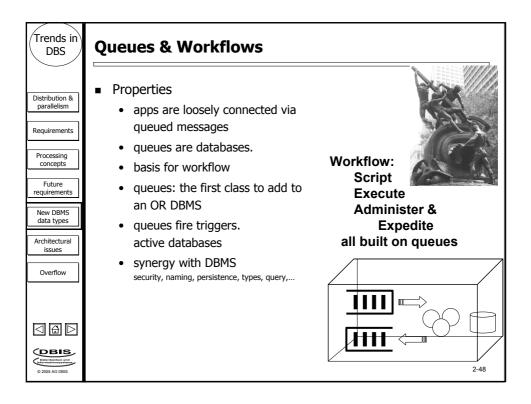


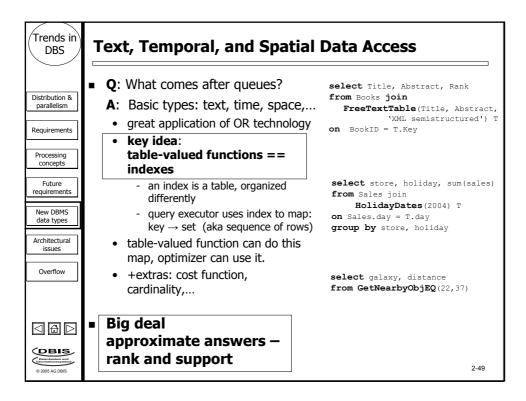


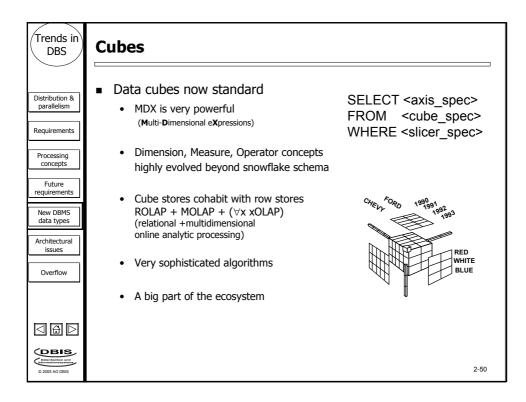


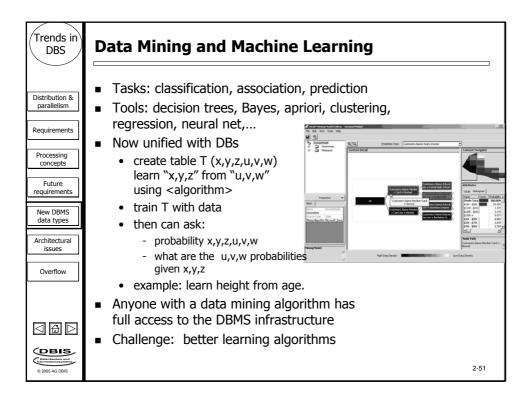


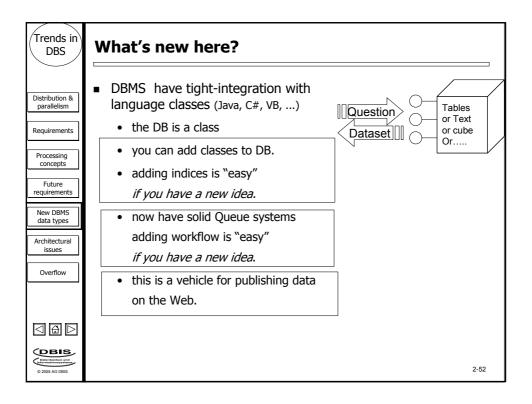


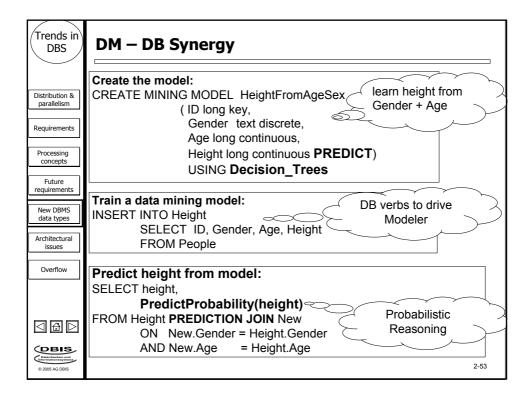


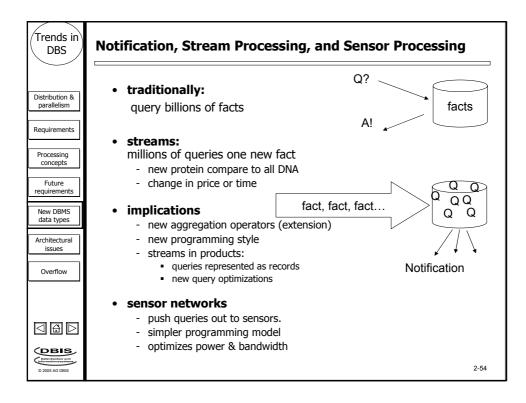


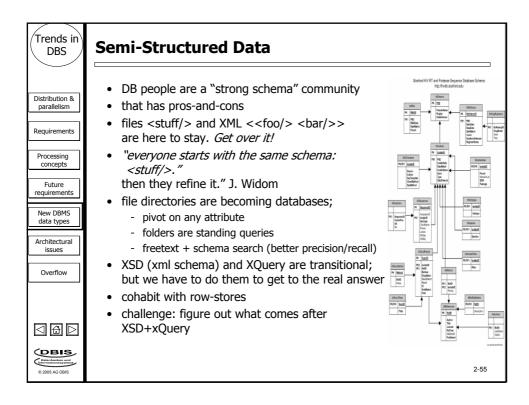


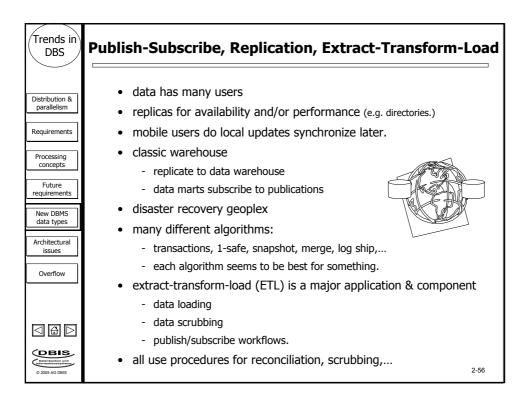


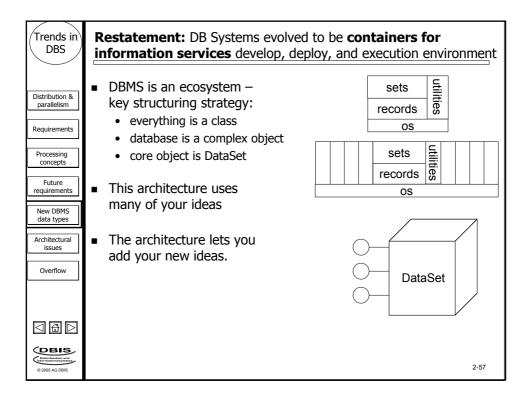


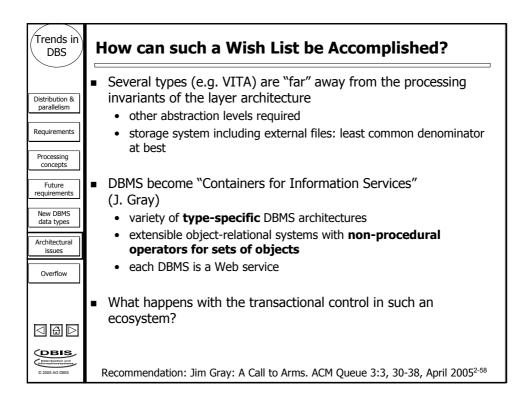


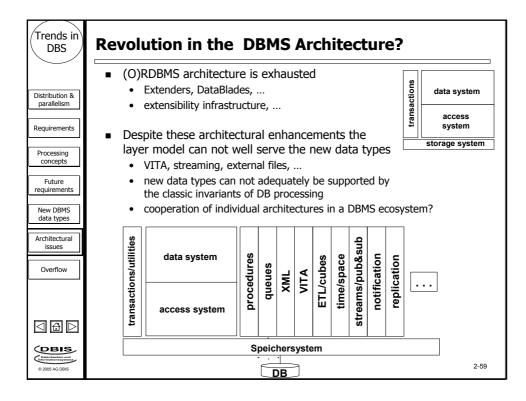


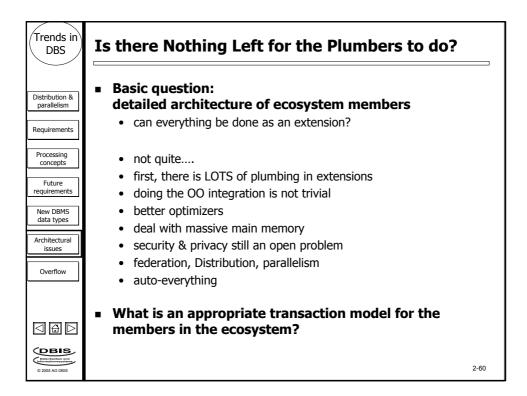


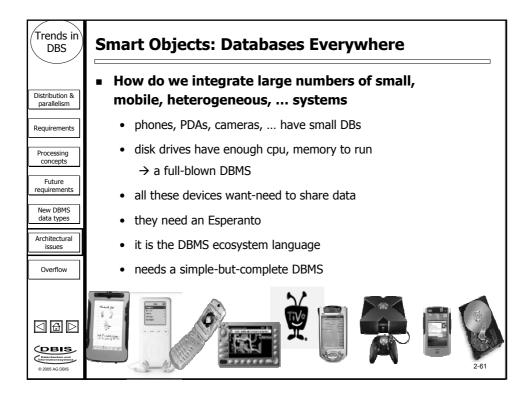


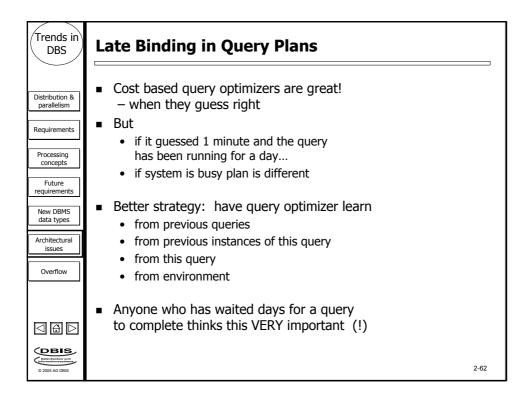


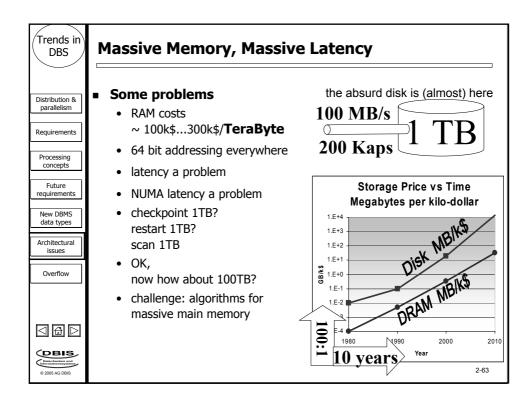


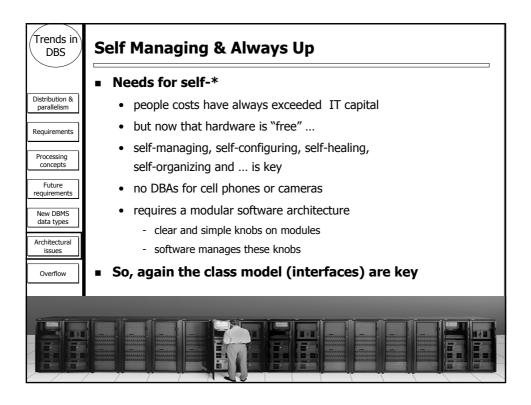




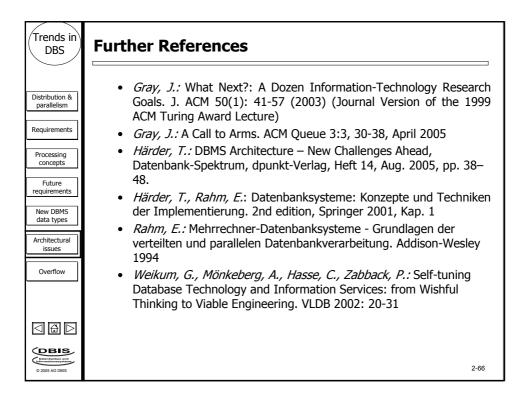


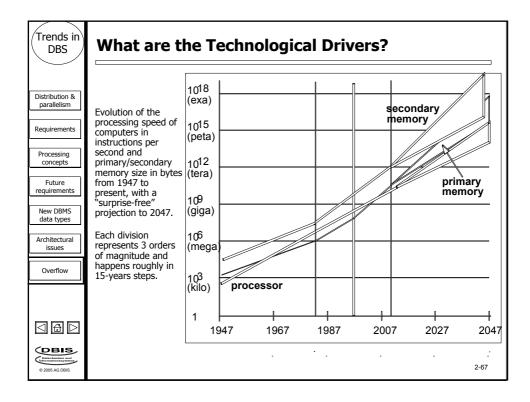


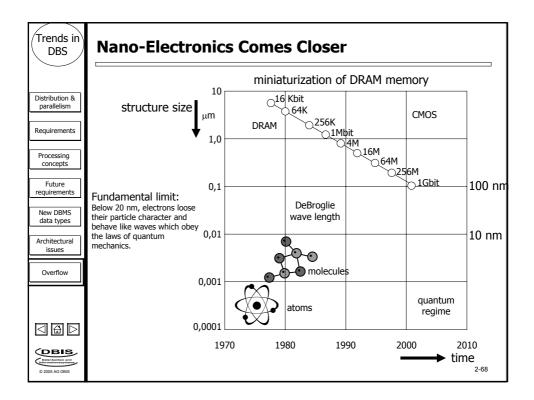


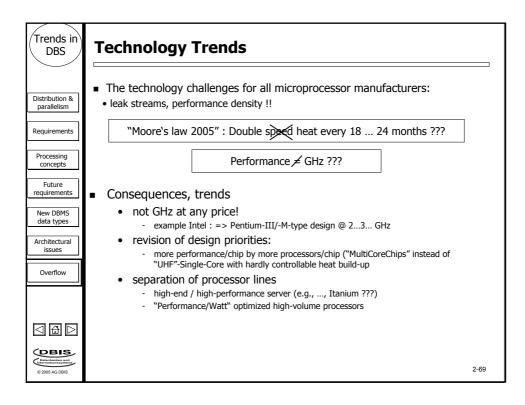


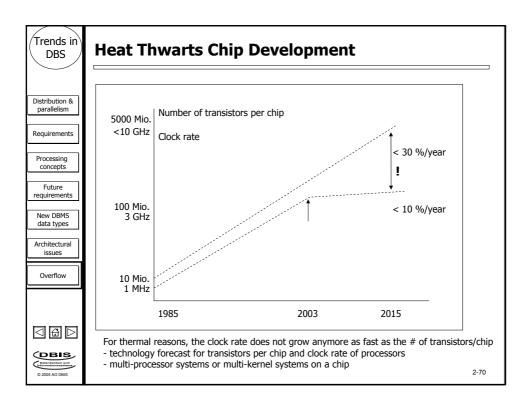
Trends in DBS	Summary	
Distribution & parallelism Requirements Processing concepts Future requirements New DBMS data types Architectural issues Overflow Overflow	 DB system properties high performance (high transaction rates, parallel processing of complex queries) support of very large DBs high availability and fault tolerance for all components modular extensibility, etc. Processing support diverse kinds of intra-transaction parallelism evaluated by speedup and scaleup metrics adaptivity in and across components GO enables Object-Relational Ecosystem federate many kinds of data enables DB extensions Yes, there are still plumbing problems left framework to allow extensions auto-everything. 	
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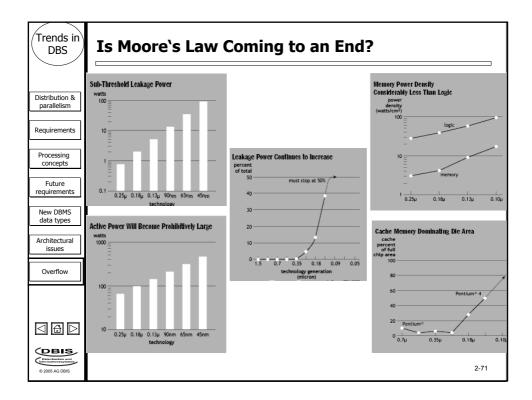


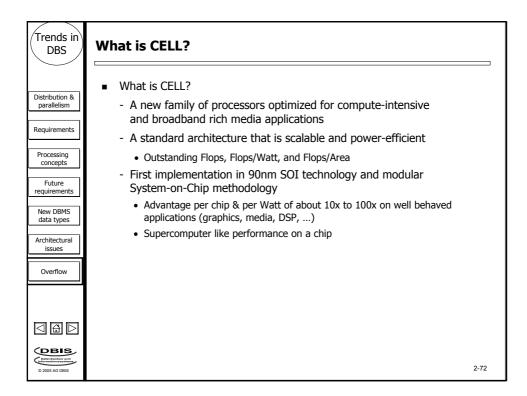


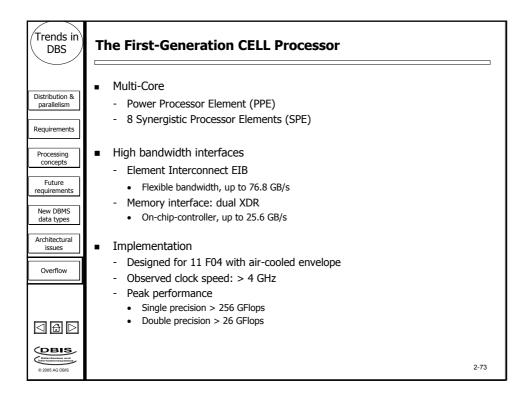












Trends in DBS	Synergistic Processor Element (SPE)	
Distribution & parallelism	Provides computational performance of CELL processor Optimized for media & streaming applications	
Requirements	 Each unit operates on 128b 4-, 8- and 16-way SIMD 	
Processing concepts	 Two instruction per cycle Dual issue to 2 pieces 	
Future requirements	■ Large 128 x 128b register file	
New DBMS data types	Shared between all unitsEnables multiple loop-unrolling	
Architectural issues	 High frequency design w/o design complexity 	
Overflow	- Software controlled Local-Store instead of Cache	
	Limited OoO to overcome dependenciesOptimized ISA	
	High bandwidth, DMA interface	
DBIS Determinations of the Information of the O 2005 AG DBIS		2-74

